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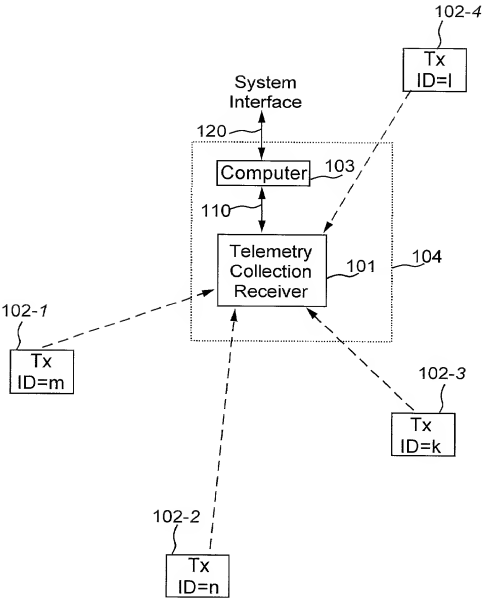

FIG. 1

000227 TTnTh260

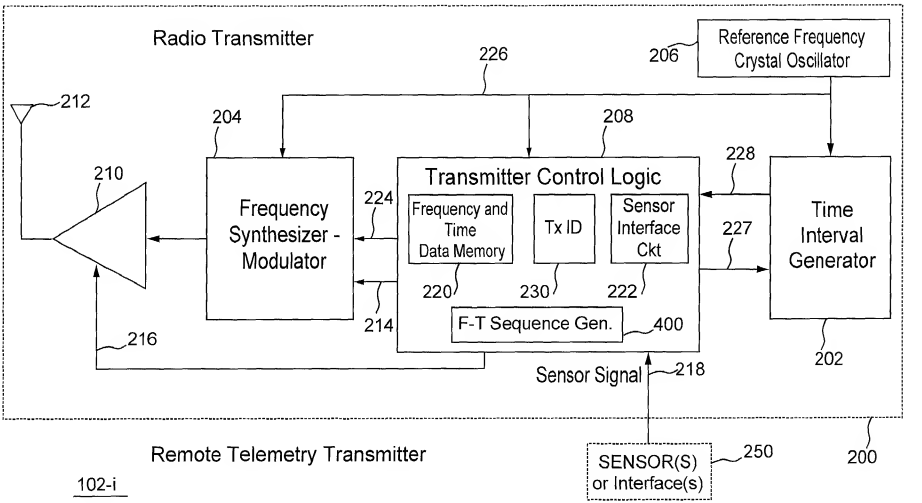

FIG. 2

Frequency/Time Sequence Generator

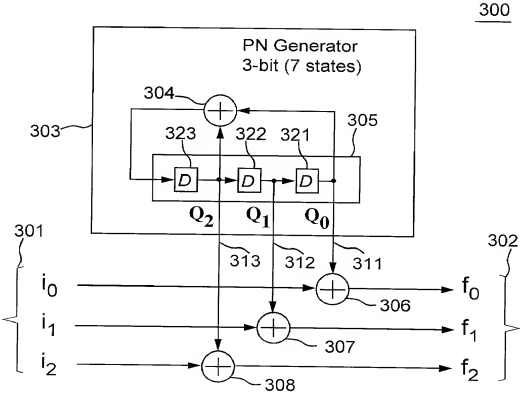

FIG. 3

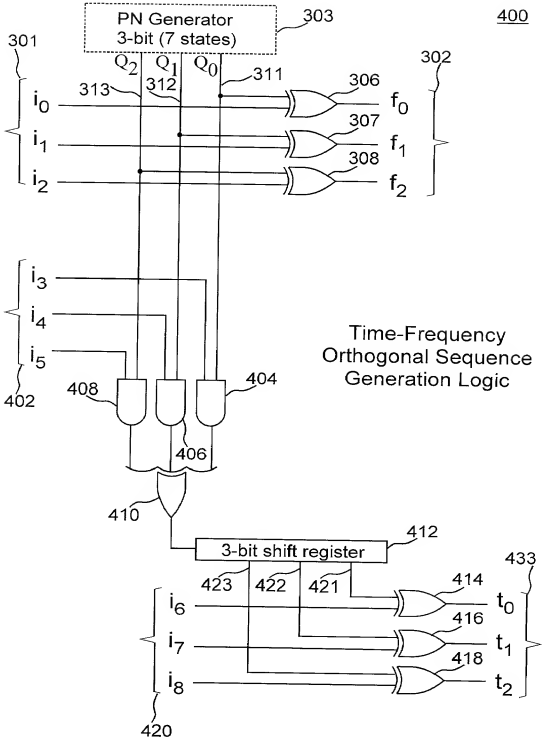
PN Generator
3-bit (7 states)
303
400
301
302
Q_2
Q_1
Q_0
313
312
311
306
307
308
i_0
i_1
i_2
f_0
f_1
f_2
i_3
i_4
i_5
402
408
404
406
410
Time-Frequency
Orthogonal Sequence
Generation Logic
412
3-bit shift register
423
422
421
433
414
416
418
i_6
i_7
i_8
t_0
t_1
t_2
420

FIG. 4

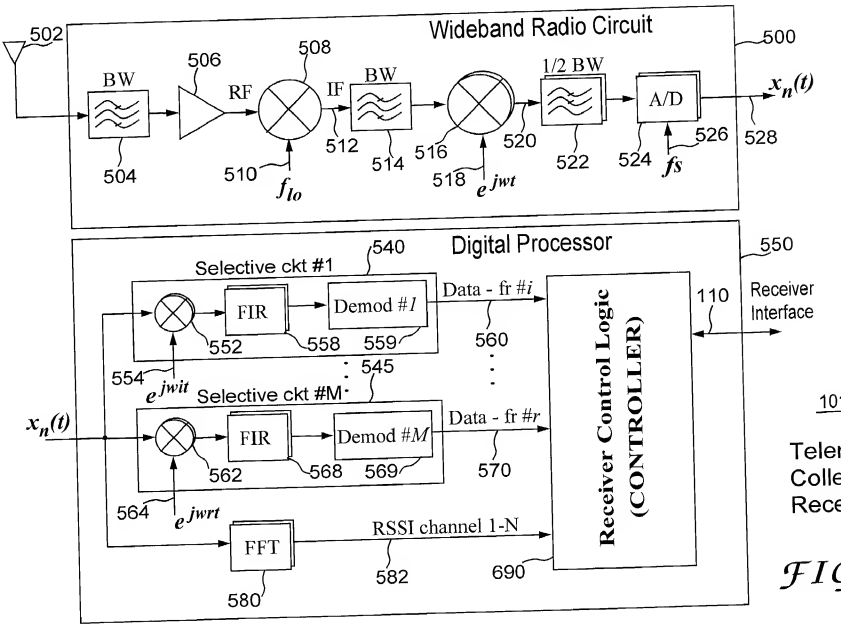
Wideband Radio Circuit
502
500
BW
504
506
RF
508
510
f_{lo}
IF
512
BW
514
516
518
e^{jwt}
1/2 BW
520
522
A/D
524
fs
526
528
$x_n(t)$
Digital Processor
550
Selective ckt #1
540
FIR
552
Demod #1
558
559
Data - fr #i
560
554
e^{jwit}
545
Selective ckt #M
FIR
562
Demod #M
568
569
Data - fr #r
570
564
e^{jwrt}
$x_n(t)$
FFT
580
RSSI channel 1-N
582
690
Receiver Control Logic (CONTROLLER)
110
Receiver Interface
101
Telemetry Collection Receiver

FIG. 5

000227 TTHTH260

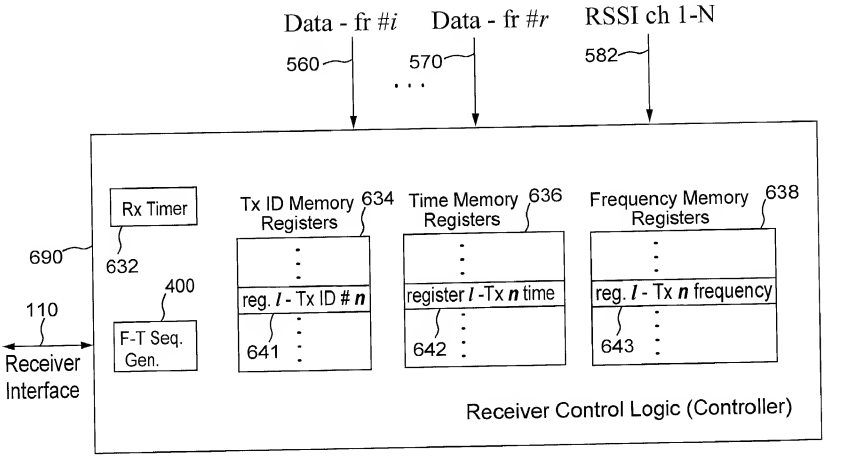

FIG. 6

000227 TEMP260

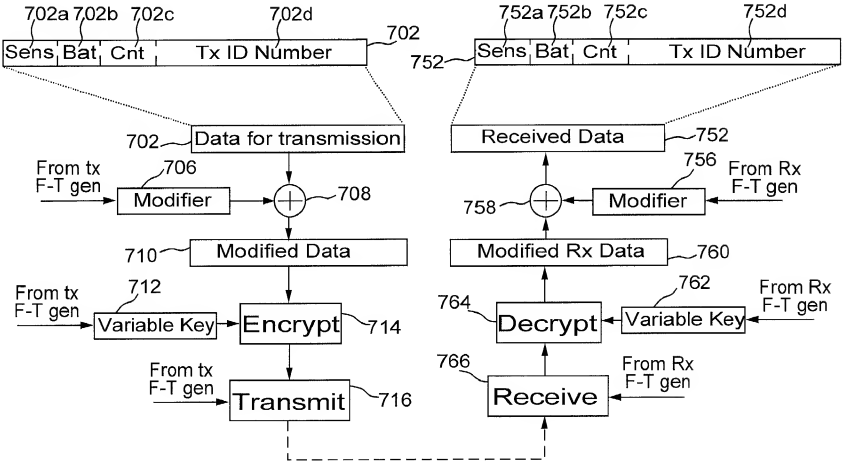

FIG. 7